

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kenneth W. Marr

Title: CIRCUITS AND METHODS FOR REPAIRING DEFECTS IN MEMORY DEVICES

Docket No.: 303.859US1
Filed: June 24, 2003
Examiner: Cynthia Britt
Customer No.: 73115

Serial No.: 10/609,312
Due Date: September 2, 2008
Group Art Unit: 2117
Confirmation No.: 8022

Notice of Allowance Date: June 2, 2008

Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

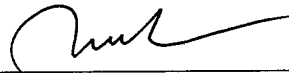
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Customer No.: 73115

Viet V. Tong

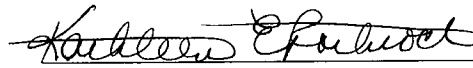
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 2 day of September, 2008.

KATHLEEN E. ROTHROCK

Name



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